Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4757	235/492,487.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/29 13:30
L2	1	1 and (inver\$4 near10 order\$1 near20 bits near20 word\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2005/06/29 13:33
L3	19	1 and ((data or information) adj1 process\$4 same micro\$1processor\$1 same communicat\$4)	US-PGPUB; USPAT	OR	OFF	2005/06/29 14:28
L4	3332	714/10,38,763,824.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/29 13:33
L5	40	4 and ((data or information) adj1 process\$4 same micro\$1processor\$1 same communicat\$4)	US-PGPUB; USPAT	OR	OFF	2005/06/29 14:28
L6	0	1 and (hardware same circuit same inver\$4)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2005/06/29 14:29
L7	4	4 and (hardware same circuit same inver\$4)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2005/06/29 14:29
L8	0	4 and (hardware same circuit same inver\$4 same card)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2005/06/29 14:29
S1	143383	(data or information) adj1 process\$4	US-PGPUB; USPAT	OR	OFF	2005/06/29 13:29
S2	1	(data or information) adj1 process\$4 same micro\$1processor\$1 same communicat\$4 same electronic\$3 same circuit\$1 same inversion\$1 adj10 bit\$1	US-PGPUB; USPAT	OR	OFF	2002/10/17 12:42
S3	1	(data or information) adj1 process\$4 same micro\$1processor\$1 same communicat\$4 same inversion\$1 adj10 bit\$1	US-PGPUB; USPAT	OR	OFF	2002/10/17 13:49

S4	2667	(data or information) adj1	US-PGPUB;	OR	OFF	2005/06/29 13:30
		process\$4 same micro\$1processor\$1 same communicat\$4	USPAT			, ,
S5	13	(data or information) adj1 process\$4 same micro\$1processor\$1 same communicat\$4 same bit\$1 same order	US-PGPUB; USPAT	OR	OFF	2002/10/17 14:16
S6	1901	(data or information) same transfer\$4 same process\$4 same micro\$1processor\$1 same communicat\$4	US-PGPUB; USPAT	OR	OFF	2002/10/17 14:23
S7	21	(smart or ic) same card same (data or information) same transfer\$4 same process\$4 same micro\$1processor\$1 same communicat\$4	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:02
S8	19	(data or information) same transfer\$4 same process\$4 same micro\$1processor\$1 same communicat\$4 same inver\$4	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:07
S9	19	(data or information) same transfer\$4 same process\$4 same micro\$1processor\$1 same communicat\$4 same inver\$4	US-PGPUB; USPAT	OR	OFF	2002/10/17 14:33
S10	1	(smart or ic) same card same (data or information) same transfer\$4 same process\$4 same micro\$1processor\$1 same communicat\$4 adj5 (device\$1 or unit\$1)	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:04
S11	4647	data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:10
S12	834	data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4 same bit\$1	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:11
S13	31	data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4 same bit\$1 same inver\$4	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:12
S14	3	data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4 same bit\$1 same inver\$4 same card\$1	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:20

615			110 202:12	00	055	2002/10/17 16 22
S15		data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4 same bit\$1 same inver\$4 same function\$1	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:38
S16	0	(smart or ic or electro\$4) adj1 card\$1 same data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4 adj1 (device\$1 or unit\$1) same bit\$1 same function\$1	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:42
S17	0	(smart or ic or electro\$4) adj1 card\$1 same data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4 adj1 (device\$1 or unit\$1) same bit\$1	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:45
S18		(smart or ic or electro\$4) same card\$1 same data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4 adj1 (device\$1 or unit\$1) same bit\$1	US-PGPUB; USPAT	OR	OFF	2002/10/17 16:51
S19	57	(smart or ic or electro\$4) same card\$1 same data same (tranfer\$4 or transmi\$4) same micro\$1processor\$1 same communicat\$4 same (device\$1 or unit\$1)	US-PGPUB; USPAT	OR	OFF	2002/10/17 17:00
S20	2	(("6299069") or ("5978822")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2002/10/18 17:57
S21	4	(("6299069") or ("5978822") or ("6377356") or ("6023705")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2002/10/18 17:58
S22	0	("hardwaresamecircuitsameinver\$ 4").PN.	US-PGPUB; USPAT; USOCR; EPO; IBM_TDB	OR	OFF	2003/06/04 07:42
S23	1209	hardware same circuit same inver\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2005/06/29 14:29
S24	27	hardware same circuit same inver\$4 same data same card\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 09:41

S25	6	card\$1 same microprocessor same inver\$4 same reader	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 09:58
S26	125	card\$1 same microprocessor same inver\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 16:20
S27	12885	"326"/\$.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 13:20
S28	1	(card\$1 same microprocessor same inver\$4) and "326"/\$.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 13:27
S29	7396	(inver\$4 same circuit\$1) and "326"/\$.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 13:28
S30	1717	235/492.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 15:51
S31	0	(235/492.ccls. and inverter) and (shift adj1 register) and filo	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 15:51
S32	34	(235/492.ccls. and inverter) and (shift adj1 register)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 17:44
S33	1	"4004133".pn. and inver\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 17:38
S34		"4004133".pn. and inver\$4 and card and reader\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 17:39
S35	0	card\$1 same reader\$1 same (microprocessor or micro-processor or (micro adj1 processor)) same inver\$4 same bit\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 17:46
S36	7	card\$1 same reader\$1 same (microprocessor or micro-processor or (micro adj1 processor)) same inver\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/04 17:46

S37	2	(conventional adj1 signal\$1) same processor same word same bits	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 12:45
S38	2	((conventional adj1 signal\$1) same processor same word same bits) same processor\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 12:46
S39	0	inver\$4 near10 order\$1 near20 bits near20 word\$1 same card\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 12:48
S40	76	inver\$4 near10 order\$1 near20 bits near20 word\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2005/06/29 13:30
S41	4	inver\$4 near10 order\$1 near20 bits near20 word\$1 same (microprocessor or micro-processor or (micro adj1 processor) or processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR ·	OFF	2003/06/06 13:23
S42	1	"4256926".pn. and order\$1 and bit\$1 and microprocessor\$1 and card\$1 and communication	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 13:24
S43	2	inver\$4 near10 order\$1 near20 bits near20 word\$1 same subscrib\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:00
S44	1	"6574776".pn. and processor\$1 and read\$3 and order\$3 and word\$1 and data and bit\$1 and inver\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:22
S45	6979	SIM or (subscriber near1 Identity near1 Module) same (microprocessor or micro-processor or (micro adj1 processor))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF .	2003/06/06 14:24
S46	6979	SIM or (subscriber near1 Identity near1 Module) same (microprocessor or micro-processor or (micro adj1 processor)) same card\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:24
S47	6978	SIM or (subscriber near1 Identity near1 Module) same (microprocessor or micro-processor or (micro adj1 processor)) same card\$1 same inver\$4 same order\$4 same bit\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:26

S48	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor)) same card\$1 same inver\$4 same order\$4 same bit\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:26
S49	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor)) same inver\$4 same order\$4 same bit\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:27
S50	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor)) same inver\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:27
S51	552	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:27
S52	293	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor\$1) same card\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:28
S53	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor\$1) same card\$1 same inver\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:29
S54	145	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor\$1) same card\$1 same communicat\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:29
S55	95	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor\$1) same card\$1 same communicat\$4 same (data or bit\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 15:46

S56	1	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor\$1) same card\$1 same communicat\$4 same (data or bit\$1) same order\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 14:31
S57	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor\$1) same card\$1 same (right or left) same shift same register\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 15:46
S58	1	"5978822".pn. and left and right and switch\$3 and register\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 15:53
S59	109	235/492.ccls. and inverter	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 16:19
S60	3	(card\$1 same microprocessor same inver\$4) and (235/492.ccls. and inverter)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2003/06/06 16:20
S61	1	"6574776".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/08/03 10:35